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Form 1449*	Atty. Docket No.: 884.512US1	Serial No. 09/995,013
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	Applicant: James E. Jaussi et al.	
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**U. S. PATENT DOCUMENTS**

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**FOREIGN PATENT DOCUMENTS**

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**OTHER DOCUMENTS**

(Including Author, Title, Date, Pertinent Pages, Etc.)

<i>QJ</i>	Baker, R., et al., "CMOS: Circuit Design, Layout, and Simulation.", <u>IEEE Press</u> , 4 pages, (1998)
<i>QJ</i>	Filanovsky, I.M., "Voltage Reference Using Mutual Compensation of Mobility and Threshold Voltage Temperature Effects", <u>ISCAS 2000- IEEE International Symposium on Circuits and Systems</u> , pp. V197-V200, (May 2000)
<i>QJ</i>	Haycock, M., et al., "A 2.5Gb/s Bidirectional Signaling Technology", <u>Hot Interconnects Symposium V</u> , pp. 1-8, (Aug. 1997)
<i>QJ</i>	Lee, S.H., et al., "A Temperature and Supply Voltage Insensitive CMOS Current Reference", <u>IEICE Trans. Electron.</u> , Vol.E82-C, No. 8, 1562-1566, (August, 1999.)

Examiner

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